

SLRFLR.0009P



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	George A. Zimmerman, et al.)	Group Art Unit	Unknown
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App. No.	:	10/603,498)		
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Filed	:	June 24, 2003)		
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For	:	METHOD AND APPARATUS FOR)		
		PRECODE CROSSTALK)		
		MITIGATION)		
)		
Examiner	:	Unknown)		
)		

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1460, Alexandria, VA 22313-1450 on

January 13, 2004
C. Miller
Chad W. Miller, Reg. No. 44,943

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the mailing of the first Office Action on the merits, and as such, no fee is required in accordance with 37 C.F.R. § 1.97(b)(3).

Respectfully submitted,

Dated: 1/13/04

By: *C. Miller*
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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. SLRFLR.0009P	APPLICATION NO. 10/603,498
	APPLICANT George A. Zimmerman, et al.	
	FILING DATE June 24, 2003	GROUP Unknown

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)	
	4,359,778	11/1982	Lee				
	4,583,235	04/1986	Dömer, et al.				
	6,088,827	07/2000	Rao				
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	6,356,555 B1	03/2002	Rakib, et al.				

FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	Oscar Agazzi, et al., "10Gb/s PMD Using PAM-5 Trellis Coded Modulation", Broadcom, <i>IEEE</i> 802.3, Albuquerque, New Mexico, March 6-10, 2000, 38 pages	
	Jamie E. Kardontchik, "4D Encoding in Level-One's Proposal for 1000BAS-T", Advanced Micro Devices, August 21, 1997 - Rev. B, pp. 1-24	
	Erich F. Haratsch, et al., "A 1-Gb/s Joint Equalizer and Trellis Decoder for 1000BASE-T Gigabit Ethernet", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 36, No. 3, March 2001, pp. 374-384	
	Mehdi Hatamian, et al., "Design Considerations for Gigabit Thernet 1000Base-T Twisted Pair Transceivers", IEEE 1998 Custom Integrated Circuits Conference, pp. 335-342	
	Gottfried Ungerboeck, "Trellis-Coded Modulation with Redundant Signal Sets", <i>IEEE Communications Magazine</i> , February 1987, Vol. 25, No. 2, pp. 5-21	
	"Gigabit Ethernet Over Category 5", Copyright 2000-2001 Agilent Technologies, 12 pages	

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	M.P. Sellers, et al., "Stabilized Precoder for Indoor Radio Communications", <i>IEEE Communications Letters</i> , Vol. 4, No. 10, October 2000, pp. 315-317
	"Wirescope 350 - Understanding ELFEXT", © 2000 Agilent Technologies, 2 pages
	David Crawford, "Adaptive Filters", © David Crawford 1996, pp. 1-5
	David A. Johns, et al., "Integrated Circuits for Data Transmission Over Twisted-Pair Channels", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 3, March 1997, pp. 398-406
	Prof. David Johns, University of Toronto, "Equalization", © D.A. Johns 1997, 29 pages
	David Smalley, "Equalization Concepts: A Tutorial", Atlanta Regional Technology Center, Texas Instruments, pp. 1-29, October 1994
	Shao-Po Wu, et al., "FIR Filter Design via Spectral Factorization and Convex Optimization", to appear as Chapter 1 of <i>Applied Computational Control, Signal and Communications</i> , Biswa Datta Editor, Birkhauser, 1977, pp. 1-33
	Richard D. Wesel, et al., "Achievable Rates for Tomlinson-Harashima Precoding", <i>IEEE Transactions on Information Theory</i> , Vol. 44, No. 2, March 1998, pp. 824-831
	Wolfgang H. Gerstacker, et al., "Blind Equalization Techniques for xDSL Using Channel Coding and Precoding", submitted to <i>AEÜ Int. J. Electr. Commun.</i> , pp. 1-4, May 1999
	Chip Fleming, "A Tutorial on Convolutional Coding with Viterbi Decoding", © 1999-2002, Spectrum Applications, pp. 1-6
	"ELFEXT - Introduction", Fluke Networks™, ©2000, pp. 1-2
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	Hiroshi Harashima, et al., "Matched-Transmission Technique for Channels with Intersymbol Interference", <i>IEEE Transactions on Communications</i> , Vol. COM-20- No.4, August 1972, pp. 774-780
	M. Tomlinson, "New Automatic Equaliser Employing Modulo Arithmetic", <i>Electronic Letters</i> , Vol. 7, 1971, pp. 138-139
	Peter Kabal, et al., "Partial-Response Signaling", <i>IEEE Transactions on Communications</i> , Vol. COM-23, No. 9, September 1975, pp. 921-934
	Robert F. H. Fischer, et al., "Dynamics Limited Precoding, Shaping, and Blind Equalization for Fast Digital Transmission over Twisted Pair Lines", <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 13, No. 9, December 1995, pp. 1622-1633
	Robert F. H. Fischer, et al., "Comparison of Precoding Schemes for Digital Subscriber Lines", <i>IEEE Transactions on Communications</i> , Vol. 45, No. 3, March 1997

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"28.5 Protocol Implementation Conformance Statement (PICS) Proforma for Clause 28, Physical Layer Link Signaling for 10 Mb/s, 100 Mb/s and 1000 Mb/s Auto-Negotiation on Twisted Pair", *IEEE Std. 802.3*, 1998 Edition, pp. 6-14 and 18-44

EXAMINER

DATE CONSIDERED

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.